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Title : Optical Metro 3500

Operations and

Maintenance

Version: Demo

1.A client has a three-node UPSR network with an OC-12 line rate. What happens to traffic if a signal degrade exceeding 10E-3 bit errors is detected on OC12-12 of Network Element (NE) 1?

A.Add/Drop traffic is lost at NE 1. There is no traffic lost on NE 2 and 3.

B.Add/Drop is selected from OC12-12 at NE 1. NE 2 and 3 select passthrough traffic from NE 1.

C.Add/Drop traffic is selected from OC12-11 at NE 1. NE 2 and 3 select passthrough traffic from NE 1.

D.Add/Drop traffic is selected from OC12-11 at NE 1. NE 2 and 3 do not select passthrough traffic from NE 1.

Answer:D

2.The Optical Metro 3500 supports multiple security access levels. There are five User Privilege Code (UPC) security levels that allow a range of task execution capabilities. Which minimum level would allow complete access to all commands?

A.Level 1

B.Level 2

C.Level 3

D.Level 4

Answer:D

3.A third node is added to an existing Optical Metro 3500 ring. What happens if the new node is not added to the Network Processor's (NP) Span of Control (SOC)?

A.The new node is automatically added to the NP's SOC.

B.Traffic on the network is affected until the new node is added to the SOC.

C.The new node can be provisioned through the NP, but only with a user account User Privilege Code (UPC) of level 5.

D.The new node will not be automatically monitored through the NP until the new node is added to the SOC.

Answer:D

4. Which statement about the Optical Metro 3500 Shelf Processor (SP) is true?

A.The SP sits in the Optical Metro 3500 slot 16.

B.The SP provides the Ethernet hubbing functionality required to interconnect Optical Metro 3000 shelves.

C.The SP uses a diskless storage media to store the software load and to record the network element provisioning and history.

D.Four types of SP are available: Shelf Processor (standard), SP Enhanced Shelf Processor SPe Extended, Shelf Processor SPx and Shelf Processor Super (SPs).

Answer:C

5. How many network elements in a Network Processor's (NP) Span of Control (SOC) are supported?

8.A

B.16

C.24

D.32

Answer:B